



MIPS™ developers to benefit from Ashling's new Real-Time Emulation and Debug tools

Limerick, Ireland and Sunnyvale, California, August 27 2001: Developers of embedded products that use the MIPS™ core can now use an integrated, scalable range of products for emulation, debugging and validation, following the introduction of the Genia and Opella Emulators by Ashling Microsystems. Developed with the co-operation of MIPS Technologies, Inc., the new products bring Ashling's Universal 32-bit Debug Platform to the fast-growing community of developers using the MIPS™ RISC cores.

The new Ashling Genia Networked Emulator connects to the on-chip MIPS EJTAG™ debug port on the target system, and incorporates high-speed Ethernet and USB connections to the host. The entry-level Opella Emulator offers debug control with a parallel port connection to the host.

Commenting on the new MIPS™ toolset, Jan Beekman, Ashling's Marketing VP, said "Developing embedded applications based on the MIPS™ cores is now fast, secure and predictable, with the introduction of support on Ashling's Universal 32-bit Debug Platform. Hardware and software developers and system-integration engineers will use this scalable, high-performance environment to debug and validate their source code, real-time kernel and coprocessors".

Together with Ashling's flagship "PathFinder" Source Debugger, the new Ashling MIPS™ debug tools provide run/stop control of the target application, single stepping, function step-into and step-over. To give developers visibility on their real-time target application from both Source-code and Assembly-instruction perspectives, PathFinder for the MIPS™ architecture includes synchronized displays of executing source-code and the corresponding assembly application code. PathFinder also allows examination and modification of target processor registers and read/write access to both on-chip and target system memory. To support the many developers of portable and battery-operated MIPS™ applications, the new Genia and Opella Emulators support low-voltage target interfaces, with automatic sensing of target operating voltage levels and full target reset control.

The PathFinder debugger supports all MIPS™ C compilers, including Green Hills Software, GNU, Wind River Systems (Diab Data) and MetaWare. To aid in integrating real-time kernels with MIPS™ applications, PathFinder supports RTOS system-level debugging.

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Note to Editors: A photo is available to accompany this announcement

Ashling Microsystems:

Ashling Microsystems (founded in 1985) designs and manufactures In-Circuit Emulators, JTAG/BDM Emulators, Smart Card development tools, source debuggers and Software Quality Assurance tools. Ashling's major-account customers include Philips, Delphi Delco, Alcatel, IBM, Gemplus, Xerox, Sony, Lexmark, Visa, Infineon, British Telecom, and SchlumbergerSema.

Ashling's R&D Center is in Limerick, Ireland. The company's North America Sales and Support Center is in Sunnyvale, California, with sales subsidiaries in the United Kingdom and France and distributors throughout the world.

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