Ashling RiscFree™ adds support for the new Cacheable Overlay Manager for RISC-V

August 12, 2020 – Redwood Shores, CA and Limerick, Ireland. Ashling a leading provider of embedded development tools today announced full tools support for the new Cacheable Overlay Manager for RISC-V (ComRV) as contributed by Western Digital to the RISC-V community.

ComRV allows large, feature-rich programs but only requires a small amount of RAM (executable memory). Functions and data may reside at the same RAM addresses during different points in the execution of user programs and this is handled *automatically* by the supplied ComRV run-time library.

ComRV is a very useful option for RISC-V based designs with strict memory size and power consumption requirements, especially for RISC-V designs that do not include an MMU or high-end operating system.

Ashling’s RiscFree™ IDE and Debugger provides full support for developing and debugging ComRV based designs from within a single software environment using a single debug probe.

For more information: [https://www.ashling.com/ashling-riscv/](https://www.ashling.com/ashling-riscv/)

**About RISC-V**
RISC-V open architecture ISA is under the governance of the RISC-V Foundation. Visit [https://riscv.org](https://riscv.org) for more details

**About Ashling**
Ashling is a world-class technology partner offering integrated solutions, tools, and design services that are at the heart of the embedded environment. Through its close cooperation with leading semiconductor vendors, Ashling has become a leader in the Embedded Software Development Tools market. Its solutions are used by engineers across a diverse range of applications from automotive and aerospace to healthcare and IoT. Ashling’s HQ and Engineering centre is in Limerick, Ireland. Ashling has sales and support representatives worldwide. For more information on Ashling see: [www.ashling.com](http://www.ashling.com).

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