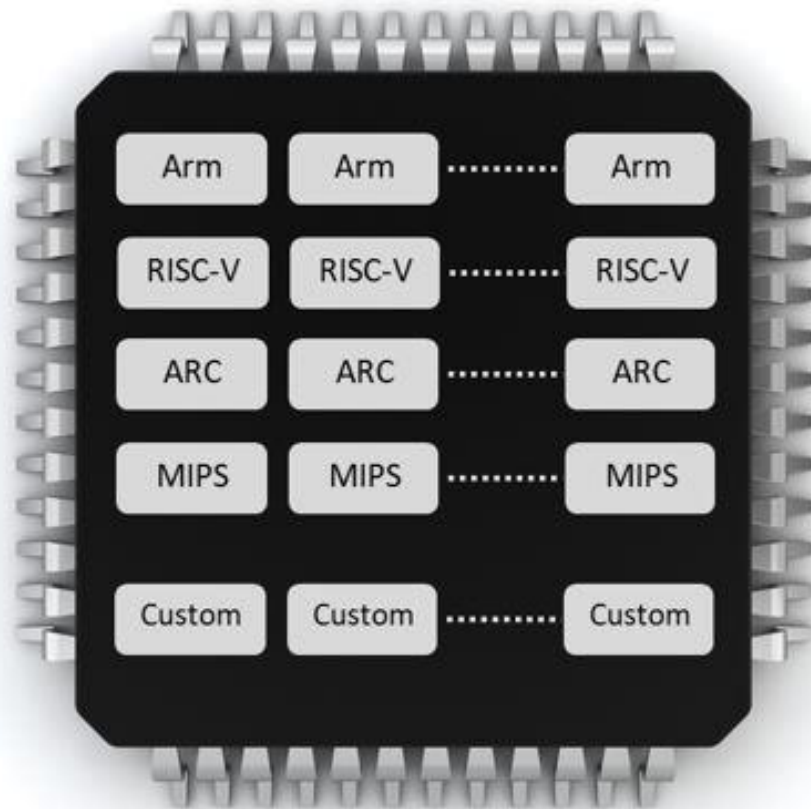


Different Cores, One Solution

July 20th, 2021 – LIMERICK, IRELAND.

Press Release



Ashling *RiscFree* “Different Cores, One Solution” for SoC, FPGA and ASIC debug

A single instance of our *RiscFree*[™] IDE supports any number of heterogeneous and homogeneous cores. You can debug all your cores (in a SoC, FPGA or ASIC) via a single debug interface (JTAG, cJTAG, SWD etc). See here for more info: <https://www.ashling.com/ashling-riscfree/>

***RiscFree*[™] IDE**, Ashling’s professional, commercial grade software development environment includes support for embedded 32-bit and 64-bit Arm, RISC-V, Synopsys ARC and MIPS based devices. The Eclipse based IDE is a seamless environment for software development from writing and building code to simulating and hardware debugging. ***RiscFree*[™] IDE** has heterogeneous multicore debug capabilities and can be used together with Ashling’s Opella-XD hardware debug probe and OpenOCD compatible probes.

Press Release

About Ashling Microsystems

Ashling Microsystems is an international Embedded Software Solutions and Debug Tools company, providing high-performance software and hardware tools for a range of embedded 32-bit and 64-bit architectures. Through its close cooperation with leading semiconductor vendors, Ashling is a world leader in the Embedded Software Development Tools market. Ashling's development centre is in Limerick Ireland. Ashling has sales and support representatives worldwide. Visit www.ashling.com for more details.

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