



New low-cost *Opella-LD* Debug Probe now available from Ashling

December 4th, 2020 – LIMERICK, IRELAND.

Ashling announces immediate availability of *Opella-LD*, a new low-cost hardware Debug Probe for embedded development.

Opella-LD supports debug of target architectures based on Arm, RISC-V or Synopsys ARC cores. It supports JTAG, cJTAG and SWD interfaces and is OpenOCD compatible. *Opella-LD* can be used together with either Ashling's *RiscFree™* Eclipse-based IDE/Debugger or the GNU GDB Debugger.

Ashling provides a dedicated support page at www.ashling.com/Opella-LD/ which contains the *Opella-LD* getting started guide, drivers and OpenOCD configuration files for all supported target architectures.

RiscFree™ IDE, Ashling's professional, commercial grade software development environment includes support for embedded 32-bit and 64-bit Arm and RISC-V based devices. The Eclipse based IDE for RISC-V development is a seamless environment for software development from writing and building code to simulating and hardware debugging. *RiscFree™ IDE* has heterogeneous multicore debug capabilities and can be used together with Ashling's *Opella-XD*, *Opella-LD* and OpenOCD compatible Debug Probes.

About Ashling Microsystems Ashling Microsystems is an international Embedded Software Solutions and Debug Tools company, providing high-performance software and hardware tools for a range of embedded 32-bit and 64-bit architectures. Through its close cooperation with leading semiconductor vendors, Ashling is a world leader in the Embedded Software Development Tools market. Ashling's development centre is in Limerick Ireland. Ashling has sales and support representatives worldwide. Visit www.ashling.com

Contact Róisín O'Keeffe,
VP Global Business Development
Email: roisin.okeeffe@ashling.com

Press Release