**Vitra-XS** is Ashling’s Debug & Trace Probe for embedded development with support for multiple target architectures including RISC-V, Arm, MIPS eVocore & Synopsys ARC powered systems.

**Vitra-XS** works with Ashling’s *RiscFree™* SDK for advanced embedded system debugging, tracing, profiling & analysis.

**Features**

- Fast, trouble-free “plug-&-play” installation using *SuperSpeed* USB3.0.
- Supports RISC-V debug & trace standards including JTAG, E-Trace & N-Trace (including SiFive Insight Trace and Debug IP).
- Supports Arm CoreSight™ debug & trace standards including SWD, DAP, ETM, PTM, STM & CTI.
- Supports Synopsys ARC Real-Time Trace (RTTv2 or greater with 8-bit or dual 8-bit ports).
- Heterogeneous (e.g., Arm + RISC-V) & homogeneous debug & trace support for multi-core SoCs sharing a single debug & trace interface.
- “Soft” *Vitra-XS* FPGA/firmware-based design allows rapid adaptation and configuration for different trace protocols including non-standard.
- 38-way Micor target connector support with adapter support for other connector types.
- Detects & automatically configures for the appropriate target voltage (from 1.2V to 3.3V).
- Automatic trace clock & data skew adjustment (“AUTOLOCK”) to ensure integrity of captured high-speed data. *Vitra-XS* automatically calibrates itself to your target’s trace data port.
- Parallel (up to 16-bits data & additional control) trace capture up to 400MHz.
- On-board trace storage memory (up to 512MB) which may be configured as a circular buffer to allow continuous trace capture up to a defined event (e.g., a breakpoint).
- Unlimited trace storage supported via high-speed, real-time streaming over *SuperSpeed* USB to host PC hard disk.
- Optional Gigabit Ethernet interface & compact form factor.